

FIG. 1

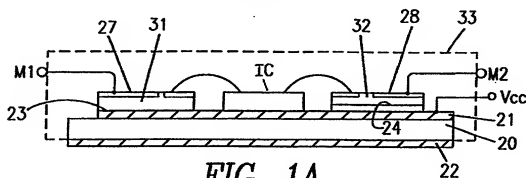


FIG. 1A

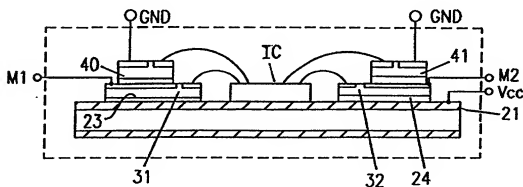


FIG. 1B

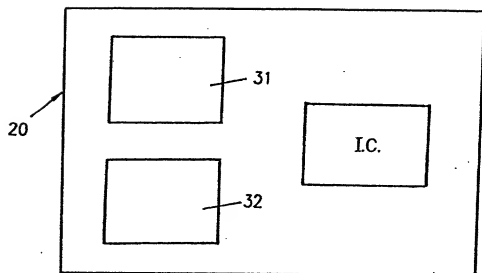


FIG. 1C

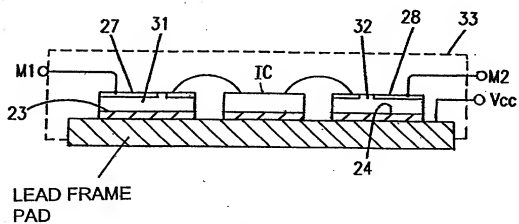


FIG. 1D



FIG. 2

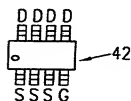


FIG. 3



FIG. 4

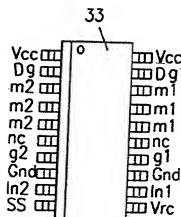


FIG. 5

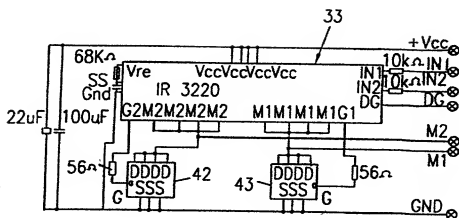
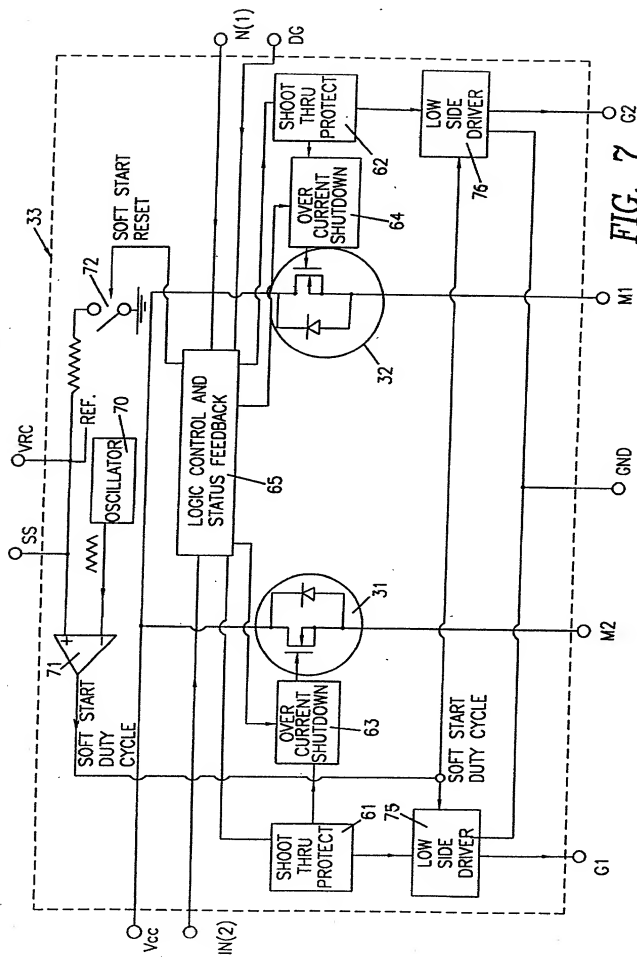


FIG. 6



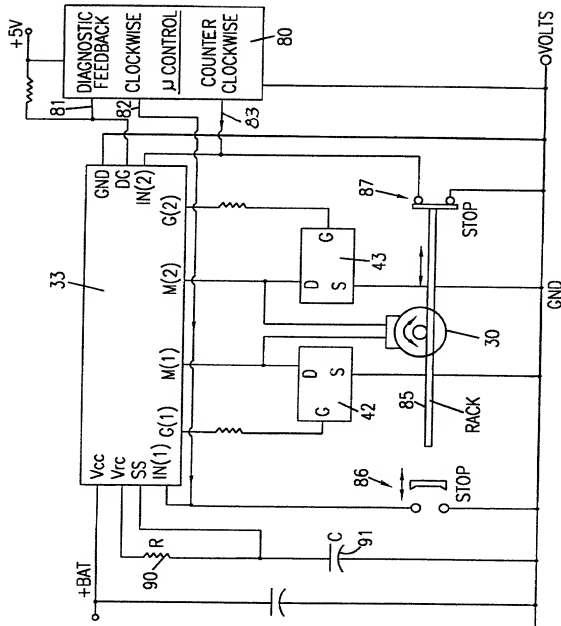
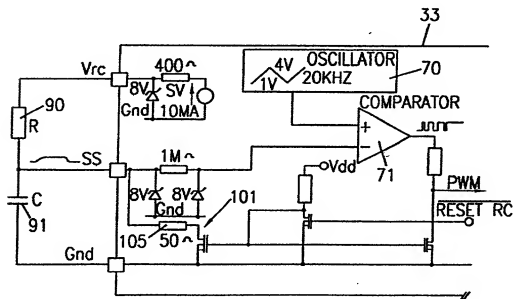
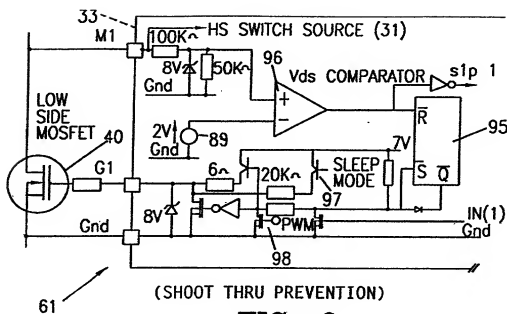


FIG. 8



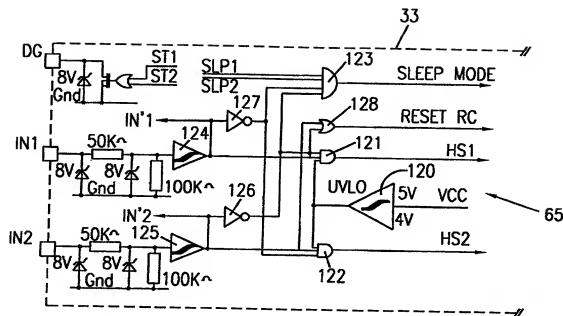


FIG. 11

SHUT DOWN CIRCUITS

FIG. 12

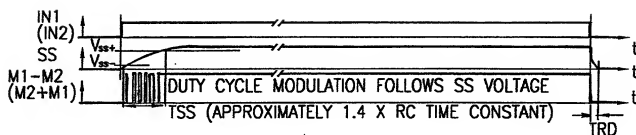


FIG. 13

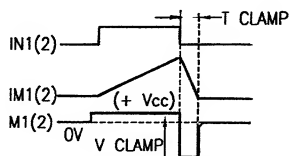


FIG. 14

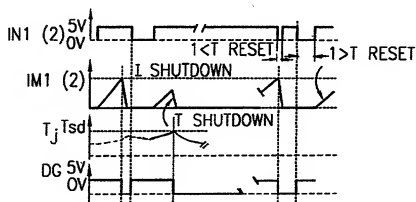


FIG. 15

FIG. 17

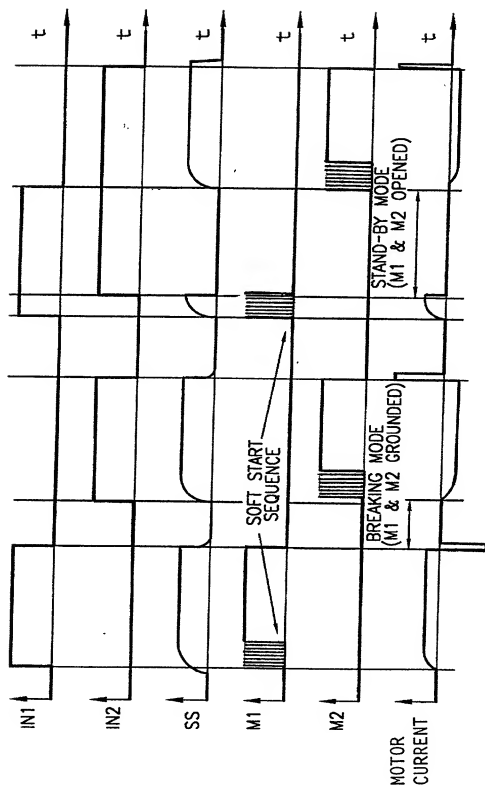


FIG. 18